

## Изследване на усилвател с програмируемо усилване с PSpice

### Съдържание на входния файл с описание на схемата и задание за анализ

```
**** CIRCUIT DESCRIPTION
*****
** Creating circuit file "pga-schematic1-s.sim.cir"
**Libraries:
** Local Libraries :
** From [PSPICE NETLIST] section of C:\Program Files\OrcadLite\PSpice\PSpice.ini file:
.lib "nom.lib"
**Analysis directives:
.TRAN 0 40u 0 0.5u
.PROBE V(*) I(*) W(*) D(*) NOISE(*)
.INC ".\pga-SCHEMATIC1.net"
**** INCLUDING pga-SCHEMATIC1.net ****
* source PGA
X_U1      1 N00711 V+ V- 2 uA741
V_V3      V+ 0 12Vdc
R_R41     N060401 N00711 100
V_V31     5 0 PULSE 0 1 20u 0.1u 0.1u 10u 40u
V_V4      V- 0 -12Vdc
V_V21     4 0 PULSE 0 1 10u 0.1u 0.1u 10u 40u
X_S1     3 0 N000452 2 SCHEMATIC1_S1
R_R11     N000452 N00711 100
V_V1      1 0 PULSE 0 10mV 2.5u 0.01u 0.01u 5u 10u
V_V41     6 0 PULSE 0 1 30u 0.1u 0.1u 10u 40u
R_R1      7 2 2k
R_R4      0 9 500
X_S4     6 0 N060401 9 SCHEMATIC1_S4
R_R3      9 8 500
X_S3     5 0 N047482 8 SCHEMATIC1_S3
X_S2     4 0 N028292 7 SCHEMATIC1_S2
R_R31     N047482 N00711 100
R_R2      8 7 1k
R_R21     N028292 N00711 100
V_V11     3 0 PULSE 0 1 0 0.1u 0.1u 10u 40u
.subckt SCHEMATIC1_S1 1 2 3 4
S_S1      3 4 1 2 _S1
RS_S1     1 2 1G
.MODEL    _S1 VSWITCH Roff=1e6 Ron=1.0 Voff=0.0V Von=1.0V
.ends SCHEMATIC1_S1
.subckt SCHEMATIC1_S4 1 2 3 4
S_S4      3 4 1 2 _S4
RS_S4     1 2 1G
.MODEL    _S4 VSWITCH Roff=1e6 Ron=1.0 Voff=0.0V Von=1.0V
.ends SCHEMATIC1_S4
.subckt SCHEMATIC1_S3 1 2 3 4
S_S3      3 4 1 2 _S3
RS_S3     1 2 1G
.MODEL    _S3 VSWITCH Roff=1e6 Ron=1.0 Voff=0.0V Von=1.0V
.ends SCHEMATIC1_S3
.subckt SCHEMATIC1_S2 1 2 3 4
S_S2      3 4 1 2 _S2
RS_S2     1 2 1G
.MODEL    _S2 VSWITCH Roff=1e6 Ron=1.0 Voff=0.0V Von=1.0V
.ends SCHEMATIC1_S2
**** RESUMING pga-schematic1-s.sim.cir ****
.END
```